**Laboratory Report Cover Sheet   
DeVry University  
College of Engineering and Information Sciences**

**Course Number:** ECET330

**Professor:**

**Laboratory Number: 1 (Part 1)**

**Laboratory Title:** Introduction to Memory Map

**Submittal Date:** Click here to enter a date.

***Objectives:***

***Results:***

***Conclusions:***

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| ***Team:*** |  |  |  |  |  |
|  | Name |  | Program |  | Signature |

***Observations/Measurements:***

IV. 1: Memory Address Ranges

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Memory Unit** | **A15** | **A14** | **A13** | **Lower Limit** | **Upper Limit** | **Size in Bytes** |
| *RAM1* | 0 | 0 | 0 | $0000 | $1fff | 8K |
| *RAM2* |  |  |  |  |  |  |
| *RAM3* |  |  |  |  |  |  |
| *RAM4* |  |  |  |  |  |  |

IV. 2 How does the memory system of Figure 1 react to these values?

IV. 3 Explain which memory unit in Figure 1 is selected.

IV. 4. How much on-chip RAM memory do we have for the MC9S12G128 microcontroller?

***Questions:***

1. Give two differences between EEPROM and Flash memory.
2. Indicate the use of each memory in the microcontroller. In other words, state which one is used for code, program variables, and variables that must remain when the power is turned off.
3. State the number of address and data pins for 32Kx8 SRAM.
4. State the number of address and data pins for 8Kx8 SRAM memory.